

Notice of References Cited	Application/Control No. 10/774,536	Applicant(s)/Patent Under Reexamination US005960289A	
	Examiner Douglas A. Wille	Art Unit 2814	Page 1 of 1

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*	B	US-5,831,897 A	11-1998	Hodges, Robert Louis	365/156
*	C	US-5,780,899	07-1998	Hu et al.	257/335
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Hodges et al. "Analysis and Design of Integrated Circuits" 1988, McGraw-Hill pp368,369.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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